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UST-DSP-TMS320F2808 v1

DSP module



Updated: 2009-12-3

DESCRIPTION

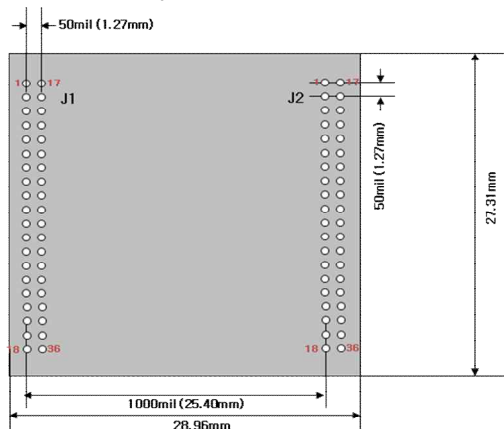
UST-DSP-TMS320F2808 is a DSP (Digital Signal Processing) module.

PROPERTIES

- Input Voltage: 2.7~10V (5V recommended)

BOARD SIZE

- Pin Header pitch: 1.27mm



BOOT MODE SELECTION

- Default: Boot to M0 SARAM
- 보드 뒷면의 저항세팅을 변경하여 사용하시면 됩니다.

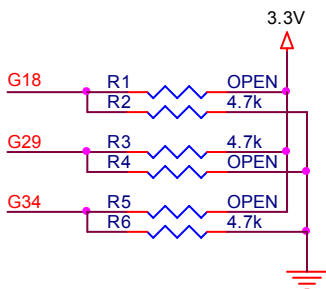


Table 3-7. Boot Mode Selection

MODE	DESCRIPTION	GPIO18 SPICLKA SCITXDB	GPIO29 SCITXDA	GPIO34
Boot to Flash/ROM	Jump to Flash/ROM address 0x3F 7FF6 You must have programmed a branch instruction here prior to reset to redirect code execution as desired.	1	1	1
SCI-A Boot	Load a data stream from SCI-A	1	1	0
SPI-A Boot	Load from an external serial SPI EEPROM on SPI-A	1	0	1
I2C Boot	Load data from an external EEPROM at address 0x50 on the I2C bus	1	0	0
eCAN-A Boot	Call CAN_Boot to load from eCAN-A mailbox 1.	0	1	1
Boot to M0 SARAM	Jump to M0 SARAM address 0x00 0000.	0	1	0
Boot to OTP	Jump to OTP address 0x3D 7800	0	0	1
Parallel I/O Boot	Load data from GPIO0 - GPIO15	0	0	0

PIN ASSIGNMENT

J1			
NO	PIN NAME	NO	PIN NAME
1	ADCINA0	19	ADCINB0
2	ADCINA1	20	ADCINB1
3	ADCINA2	21	ADCINB2
4	ADCINA3	22	ADCINB3
5	ADCINA4	23	ADCINB4
6	ADCINA5	24	ADCINB5
7	ADCINA6	25	ADCINB6
8	ADCINA7	26	ADCINB7
9	ADC_APWR	27	ADC_APWR
10	AGND	28	AGND
11	TMS	29	TRST
12	TDI	30	TDO
13	TCK	31	N.C. (Not connected)
14	EMU0	32	EMU1
15	XCLKOUT	33	/XRS
16	2.7~10V (External Power)	34	2.7~10V (External Power)
17	3.3V	35	3.3V
18	GND	36	GND

J2			
NO	PIN NAME	NO	PIN NAME
1	G0/EPWM1A	19	G18/SPICLKA/SCITXB
2	G1/EPWM1B/SPISIMOD	20	G19/SPISTEA/SCIRXB
3	G2/EPWM2A	21	G20/EQEP1A/SPISIMOC/CANTXB
4	G3/EPWM2B/SPISOMID	22	G21/EQEP1B/SPISOMIC/CANRXB
5	G4/EPWM3A	23	G22/EQEP1S/SPICLKC/SCITXB
6	G5/EPWM3B/SPICLKD/ECAP1	24	G23/EQEP1I/SPISTEC/SCIRXB
7	G6/EPWM4A/EPWMSYNCIO	25	G24/ECAP1/EQEP2A/SPISIMOB
8	G7/EPWM4B/SPISTED/ECAP2	26	G25/ECAP2/EQEP2B/SPISOMIB
9	G8/EPWM5A/CANTXB/ZADCSOCAO	27	G26/ECAP3/EQEP2I/SPICLKB
10	G9/EPWM5B/SCITXB/ECAP3	28	G27/ECAP4/EQEP2S/SPISTEB
11	G10/EPWM6A/CANRXB/ZADCSOCBO	29	G28/SCIRXDA/ZTZ5
12	G11/EPWM6B/SCIRXB/ECAP4	30	G29/SCITXDA/ZTZ6
13	G12/ZTZ1/CANTXB/SPISIMOB	31	G30/CANRXA
14	G13/ZTZ2/CANRXB/SPISOMIB	32	G31/CANTXA
15	G14/ZTZ3/SCITXB/SPICLKB	33	G32/SDAA/EPWMSYNCI/ZADCSOCAO
16	G15/ZTZ4/SCIRXB/SPISTEB	34	G33/SCLA/EPWMSYNCO/ZADCSOCBO
17	G16/SPISIMOA/CANTXB/ZTZ5	35	G34
18	G17/SPISOMIA/CANRXB/ZTZ6	36	GND

USAGE

- Connect JTAG Emulator to DSP board using JIG as the following figure.

